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Debrah K. Sim
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Borkar et al. Art Unit : 2816
Serial No.: 09/802,584 Examiner : Cassandra Cox
Filed : March 8, 2001
Title : A SYNCHRONOUS CLOCK GENERATOR FOR INTEGRATED CIRCUITS

Commissioner for Patents
Washington, D.C. 20231

RESPONSE

In response to the action mailed October, 24, 2002, please

amend the application as follows:

In the claims:

Please amend claims 1, 2, 5-7, 9-12, and 14-20 as follows:

RB C3

1. (Amended) A device, comprising:
a delay lock loop circuit responsive to an input
signal to delay the input signal by a first period and to
generate an analog control signal having an amplitude; and
a delay circuit coupled to the delay lock loop circuit
and responsive to the input signal, the delay circuit being
responsive to the analog control signal from the delay lock
loop circuit to delay the input signal by a second period
as a function of the analog control signal amplitude, the

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01 FC:1201

252.00 OP

6.225843
SKILL/GRANT
755/ACU
5/

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

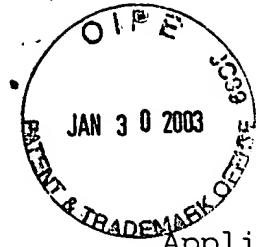
Date of Deposit

January 24, 2003

Signature

Debrah K. Sim

Debrah K. Sim
Typed or Printed Name of Person Signing Certificate



Attorney's Docket No.: 10559-412001 / P10349
2816 \$

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TRANSMITTAL LETTER

Correspondence relating to this application is enclosed.
The required fees are computed below. Please apply any charges
not covered, or any credits, to Deposit Account No. 06-1050.

Total

Claims	20	-	20	=	0	\$0
Independent	6	-	3	=	3	\$252
First Presentation of Multiple Dependent						
Claims						\$0

TOTAL FEE DUE \$252

A check for \$252 is attached.

Respectfully submitted,



Scott C. Harris
Reg. No. 32,030

Date: _____

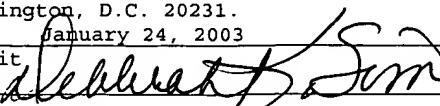
Fish & Richardson P.C.
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Debrah K. Sim

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Attorney's Docket No. 10559-412001	Express Mail Label No.	Mailing Date January 24, 2003	For PTO Use Only <i>Do Not Mark in This Area</i>
Application No. 09/802,584	Filing Date March 8, 2001	Attorney/Secretary Init SCH/dks	
Title of the Invention A SYNCHRONOUS CLOCK GENERATOR FOR INTEGRATED CIRCUITS			
Applicant Shekhar Y. Borkar et al.			
Enclosures · Transmittal Letter (1 page) · Amendment/Response (16 pages) · Check in the amount of \$252.00			